# PLDA Group and Auviz Systems Team up to Deliver FPGA-based Computer Vision Accelerators with QuickPlay, the Next-generation Software-defined FPGA Development Environment

*Integration of Auviz middleware IP with QuickPlay enabled rapid development of a Color Detection Application that will be showcased at the Embedded Vision Summit, May 2-4, 2016.*

SAN JOSE, Calif., May 2, 2016 –PLDA GROUP, a leading provider of FPGA based hardware and software solutions for the electronics industry and Auviz Systems, a leader in accelerating algorithms on FPGA’s announced today a partnership to deliver FPGA-based computer vision accelerators for OEMs and System Integrators to build differentiated FPGA-based imaging systems in record time, without hardware expertise.

FPGA’s have unique parallel computing capabilities but are complex to program. QuickPlay offers a unified Eclipse-based C/C++ system design environment where the entire FPGA accelerator design, personalization and validation is done at the software level, leveraging IP from companies like Auviz Systems. Applications can be implemented and running on FPGA development boards using QuickPlay in just a few clicks.

The availability of Auviz Systems’ middleware IP within QuickPlay allows computer vision professionals to rapidly create a broad range of leading-edge FPGA-based machine-learning and vision accelerators without requiring FPGA programming expertise.

*“It was impressive to see one of our first computer vision IP functions integrated so rapidly in the QuickPlay environment, and the entire vision system assembled and running in hardware in minutes” said Nagesh Gupta, Founder and CEO of Auviz Systems. “The demo really speaks to the capability of the QuickPlay development environment for creating complex end-to-end vision accelerators in record time, and multiplies the value of our Middleware IP for this market”.*

QuickPlay enables developers of computer vision applications to further personalize their accelerators by integrating their own value-add IP, whether developed in C or in HDL, and even re-architect the accelerator design, partially or completely, to meet new requirements.

*“Auviz Systems has demonstrated their leadership in computer vision and the integration of their IP is an important milestone for QuickPlay’s adoption in this market,” said Arnaud Schleich, President and CEO of PLDA GROUP… “We are looking forward to having other elements of the Auviz Systems middleware library integrated such as their Deep Neural Network IP, and enabling new levels of acceleration for computer vision”.*

**About the demo**

*A 10GigE Vision based image processing application utilizing color detection will be showcased at the QuickPlay booth T20 at the Embedded Vision Summit, May 2-4, 2016.*

**About Auviz Systems**

Auviz Systems is the technology leader in accelerating algorithms for FPGAs. For more information, visit [www.auvizsystems.com](http://www.auvizsystems.com/)

**About QuickPlay**

QuickPlay ([www.quickplay.io](http://www.quickplay.io)) is a PLDA GROUP brand that aims at accelerating the adoption of FPGA-based reconfigurable hardware in IT infrastructures by opening up FPGA design to non-hardware experts. QuickPlay is the result of years of research in the field of High-Level Design (HLD) and High-Level Synthesis (HLS) combined with PLDA GROUP strong expertise in FPGA hardware and IP design. QuickPlay enables leading technology companies to rip the benefits of FPGA without the pain, in domains such as computer vision, Cloud computing, HPC, image and video processing, data center networking and more.

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